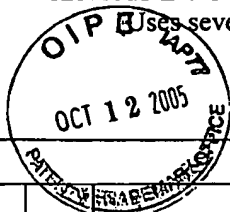


FORM PTO-1449
(REV. 7-80)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
44541/6:2APPLICATION NO.
10/822,115

SUPPLEMENTAL INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

APPLICANT
Michael C. StelowitzFILING DATE
April 8, 2004GROUP
2182

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
W	AA	5,072,378	12/10/1991	Manka et al.			
W	AB	5,608,891	3/4/1997	Mizuno et al.			
W	AC	5,765,186	6/9/1998	Searby et al.			
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AL							
	AM							
	AN							
	AO							
	AP							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

W	AR	International Search Report, PCT/US20004/011021, 3/30/2005						
	AS							
	AT							

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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER								DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
Q	AA	5	9	6	4	8	6	6		10/12/99	Durham, <i>et al.</i>	712	200	
	AB	5	7	7	1	3	7	2		6/23/98	Pham, <i>et al.</i>	395	551	
	AC	5	4	9	1	8	0	4		2/13/96	Heath, <i>et al.</i>	395	275	
	AD	5	4	7	1	5	8	1		11/28/95	Munier, <i>et al.</i>	395	250	
	AE	5	4	2	8	6	4	9		6/27/95	Cecchi	375	372	
	AF	5	3	9	2	4	2	5		2/21/95	Elliott, <i>et al.</i>	395	575	
	AG	5	2	6	8	5	9	2		12/7/93	Bellamy, <i>et al.</i>	307	43	
	AH	5	1	8	5	8	6	2		2/9/93	Casper, <i>et al.</i>	395	250	
	AI	5	1	5	1	9	7	7		9/29/92	Fredericks, <i>et al.</i>	395	200	
	AJ	5	0	3	8	3	2	0		8/6/91	Heath, <i>et al.</i>	364	900	
	AK	5	0	0	3	5	5	8		3/26/91	Gregg	375	108	
Q	AL	4	5	1	4	8	2	3		8/30/85	Mendelson, <i>et al.</i>	364	900	

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER								DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
														YES	NO
	AM														

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

Q	AN	EDN Access; for Design, by Design: "Embedded Systems," January 18, 1996, published on the Internet by <i>EDN Magazine</i> at < http://www.e-insite.net/ednmag/archives/1996/011896/02prod4.htm >.												
Q	AO	EDN Access; for Design by Design: "Easy-to-Use Flash-Memory Modules Emulate Disk Drives," September 1, 1994, by Gary Legg. Published on the Internet by <i>EDN Magazine</i> at < http://www.e-insite.net/ednmag/archives/1994/090194/18df2.htm >.												
Q	AP	Silicon Valley Computer advertisement dated January 21, 1994, by Norm Towson.												
Q	AR	"Silicon Valley Computer, IDE Mirror Card, Model ADP104/ADP108," User Manual, Part No. 113, Copyright 1992 by Silicon Valley Computer. (Cover page, table of contents, introduction, and pages 8-16.)												

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W	AS	6	2	3	7	0	5	2		5/22/01	Stolowitz	710	61	
	AT	6	0	9	8	1	1	4		8/1/00	McDonald, <i>et al.</i>	710	5	
	AU	6	0	1	8	7	7	8		1/25/00	Stolowitz	710	61	
	AV	5	5	8	1	7	1	5		12/3/96	Verinsky, <i>et al.</i>	395	309	
	AW	5	8	9	0	0	1	4		3/30/99	Long	395	828	
W	AX	5	7	9	4	0	6	3		8/11/98	Favor	395	800.23	
	AY													

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

W	AZ	Sven Schulz, "Elegant Pair Skating," Promise FastTrak, EIDE RAID Adapter, English translation of a German article (also submitted herewith) published in <i>C'T Magazin Für Computer Technik</i> , June 1998, page 116, XP002216756, Hanover Germany.
W	BA	5 January 2001 International Search Report for international application number PCT/US00/26343.
W	BB	Jin, Hai, et al., "Improving Partial Stripe Write Performance in RAID Level 5." Proceedings of the 1998 Second IEEE International Caracas Conference on Devices, Circuits and Systems, 1998. Pages 396-400.
W	BC	Sahai, Anil K., "Performance Aspects of RAID Architectures." Performance, Computing and Communications Conference, 1997. IPCCC 1997. IEEE International. Pages 321-327.
W	BD	Zabback, P., et al., "The RAID Configuration Tool," Third International Conference on High Performance Computing, 1996. Pages 55-61.
W	BE	Cheng, A.M.K., et al., "Improving the I/O Performance of Real-Time Database Systems with Multiple-Disk Storage Structures." Proceedings of the 1996 International Conference on Parallel Processing, 1996. Vol. 3., Pages 204-211.
W	BF	Feng, Dan, et al., "Performance Analysis of RAID for Different Communication Mechanism Between RAID Controller and String Controllers." IEEE Transactions on Magnetics. Vol. 32, No. 5. September 1996. Pages 3890-3892.
W	BG	Menon, Jai, et al., "The Architecture of a Fault-Tolerant Cached RAID Controller." Proceedings of the 20th Annual International Symposium on Computer Architecture, 1993. Pages 76-86.
W	BH	Specification entitled "PCI IDE Controller Specification" (revision 1.0, dated 3/4/94) (anonymous).

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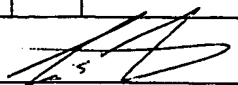
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	BI						
	BJ						
	BK						
	BL						
	BM						
	BN						
	BO						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	BP	Specification entitled "Programming Interface for Bus Master IDE Controller" (revision 1.0, dated 5/16/94), by Brad Hosler, Intel Corporation.
	BR	Specification entitled "SATA PHY Interface Specification (SAPIS). Draft rev. 0.90, February 8, 2002. Copyright Intel Corporation.
	BS	
	BT	
	BU	
	BV	
	BW	
	BX	
	BY	

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